

Time		Sunday, October 1, 2017	Room
18:00	21:00	Welcome Reception / Registration	East Concourse

Time		Monday, October 2, 2017 – Parallel Technical Session A3		Room
07:00	18:00	Registration		East Concourse
07:00	08:00	Breakfast		2FGH, 2 <sup>nd</sup> Floor
07:00	16:00	Speaker Ready Room		Exec. Conf. Ctr.
09:00	16:00	Exhibitor Trade Show		MR 1-3N, 10-13
		A3 - Session 1: Switching Phenomena <i>Chair: Frank Richter (Germany)</i>		Presenter
08:00	08:15	70	Circuit Breaker TRV Mitigation Measure for Typical Shunt and Current Limiting Air-Core Reactors	Monty Goulkhah (Canada)
08:15	08:30	44	Circuit Breaker Requirements for Out-of-Phase Switching	Anton Janssen (The Netherlands)
08:30	08:45	74	Analytical and Experimental Study on Switching Phenomena and Requirements of the Tertiary Circuit of 1000kV Transmission System	Kosuke Yanagi (Japan)
08:45	09:00	84	Controlled Switching for Transmission Line	Tomohito Mori (Japan)
09:00	09:30	Networking Break		Exhibit Area
		A3 - Session 2: Switching Equipment <i>Chair: Nenad Uzelac (United States)</i>		Presenter
09:30	09:45	69	On-Site Non-Intrusive Testing of AC Circuit-Breakers	Thomas Renaudin (Canada)
09:45	10:00	131	Conclusions of the Refurbishment of 400 kV and 220 kV Disconnectors	Per Westerlund (Sweden)
10:00	10:15	19	Switching Interruption Performance Comparison Between SF6 and Fluoroketones-Based Mixtures in HVCB	Javier D. Mantilla Florez (Switzerland)
10:15	10:30	80	Alternatives to SF6 for HV Circuit Breaker Applications	Anne Bosma (Sweden)
10:30	11:00	Networking Break		Exhibit Area
		A3 - Session 3: HV Equipment <i>Chair: Li Peng (China)</i>		Presenter
11:00	11:15	24	Application of Metal Oxide Surge Arresters in Parallel with Circuit Breaker's Chambers as a Possible Solution to Reduce TRV	Jorge Amon Filho (Brazil)
11:15	11:30	137	Electro-Thermal Simulations of High Voltage Metal-Oxide Surge Arresters With and Without Installed Grading Rings with Regard to Thermal Stability	Moritz Giebel (Germany)
11:30	11:45	62	Analysis of Equipment Requirements to Implement a 1100 kV UHV AC Transmission Line in Brazil	Ricardo Leon Vasquez-Arnez (Brazil)
11:45	12:00	25	Application Research of Fiber Optical Current Transducer - UHVDC Transmission Project	Sunan Lou (China)
11:30	13:00	CIGRÉ Canada Women in Engineering Luncheon		MR 17
12:00	13:00	Lunch		2FGH, 2 <sup>nd</sup> Floor

		A3 - Session 4: Substation and Equipment <i>Chair: Farnoosh Rahmatian (Canada)</i>		Presenter	MR-4
13:00	13:15	43	Life-Time Management of AIS Oil-Paper-insulated Instrument Transformers	Anton Janssen (The Netherlands)	
13:15	13:30	147	Current Transformer Failures in Brazilian Grid and Lifetime Management	Andreia Monteiro (Brazil)	
13:30	13:45	81	Innovative Solutions for Realization of Digital Substations	Anne Bosma (Sweden)	
13:45	14:00	106	Upgrading Existing Grounding System for Substation Expansion Projects – A Case Study	Ehsan Abbasi (Canada)	
14:00	14:30	Networking Break			Exhibit Area
17:00	18:30	Cocktail Reception			Carlton Lobby
18:30	22:00	Conference Banquet			York Ballroom 1

Time		Monday, October 2, 2017 – Parallel Technical Session B4			Room
07:00	18:00	Registration			East Concourse
07:00	08:00	Breakfast			2FGH, 2 <sup>nd</sup> Floor
07:00	16:00	Speaker Ready Room			Exec. Conf. Ctr.
09:00	16:00	Exhibitor Trade Show			MR 1-3N, 10-13
		B4 - Session 1 <i>Chair: Mohamed Rashwan (Canada)</i>		Presenter	MR-3
08:00	08:12	68	Control System Upgrade of the Quebec New England Multi-Terminal HVDC link	Hans Bjorklund (Sweden)	
08:12	08:24	71	Madawaska HVDC Refurbishment: Upgrade of Control and Protection Functionalities Using Real-Time Simulator	Chiasson Pierre-André (Canada)	
08:24	08:36	92	Reliability Enhancement of Fenno-Skan Bipole	Tuomas Rauhala (Finland)	
08:36	08:48	30	Forty Years Operational Experience and Upgrades/Refurbishment of Square Butte HVDC System	Peter Schommer (U.S.A.)	
08:48	09:00	27	Additional Functions of the Upgraded TCSR with Split Windings	Olga Suslova (Russia)	
09:00	09:30	Networking Break			Exhibit Area

		B4 - Session 2 <i>Chair: Joanne Hu (Canada)</i>		Presenter	MR-3
09:30	09:42	47	A Review on AC and DC Protection Technologies: Towards a Multivendor Solution	Mian Wang (Belgium)	
09:42	09:54	57	One-Side Spectral Method for Line Fault Location on HVDC Line	Olga Suslova (Russia)	
09:54	10:06	63	Constrained POD Controller for MMC VSC	Carl Barker (U.K.)	
10:06	10:18	132	Grid of the Future - Challenges with High Penetration of Renewables and its Impact on FACTS Devices and Synchronous Condensers	Mikael Halonen (Sweden)	
10:18	10:30	122	Extending of Lifetime with Improved Performance - Concept for Upgrade of Classic SVCs to Hybrid STATCOM	Mikael Halonen (Sweden)	
10:30	11:00	<b>Networking Break</b>			Exhibit Area
		B4 - Session 3 <i>Chair: John McNichol (Canada)</i>		Presenter	MR-3
11:00	11:12	51	Manitoba Hydro's Bipole III Transmission Project - Design Aspects and Major Technical Features	Carsten Bartzsch (Germany)	
11:12	11:24	72	Champa-Kurukshetra HVDC ±800kV, 6000MW HVDC Parallel Bipole Transmission System Main Scheme Design and Operational Considerations for Different Parallel Operation Modes	Gearoid o hEidhin (U.K.)	
11:24	11:36	76	Design Considerations for the Extension of COBRA Cable Point-to-Point HVDC Transmission Link into a Multi-Terminal System	Alex Alefragkis (Netherlands)	
11:30	13:00	<b>CIGRÉ Canada Women in Engineering Luncheon</b>			MR 17
12:00	13:00	<b>Lunch</b>			2FGH, 2 <sup>nd</sup> Floor
		B4 - Session 4 <i>Chairs: Eduardo Prieto Garcia (Spain)</i>		Presenter	MR-3
13:00	13:12	77	Site Selection and Basic Design of Belo Monte 1 HVDC Ground Electrodes	Paulo Edmundo Fonseca Freire (Brazil)	
13:12	13:24	93	Failures, Fixes and Findings: Year One of an HVDC Link	Michael Anderson (Canada)	
13:24	13:36	49	Ascutney SVC Associated with Connecticut River Valley Project	Joanne Hu (Canada)	
13:36	13:48	101	Functional and Dynamic Performance Tests of MMC HVDC Projects on Real Time Digital Simulator – Experiences in China	Hui Ding (Canada)	
13:48	14:00	135	Bipole III - DC Switchgear for Highly Flexible Bipolar HVDC Transmission schemes	Carsten Bartzsch (Germany)	
14:00	14:30	<b>Networking Break</b>			Exhibit Area

		B4 - Session 5 <i>Chair: Ahmad Al-Mubarak (Saudi Arabia)</i>		Presenter	MR-3
14:30	14:42	35	Condition Assessment of HVDC Transformer. A way to extend expected life applied to the Skagerrak transmission system	Hana Assefa (Norway)	
14:42	14:54	39	Investigation of the Thyristor Failures at Lamar HVDC Link and the Impact of Nearby Wind Farms	Mojtaba Mohaddes (Canada)	
14:54	15:06	53	Operation Experience with STATCOM in a Weak 145 kV grid	Sigmund Bødal (Norway)	
15:06	15:18	109	Gamma Control Evaluation to Enhance Commutation Failure	Tony Weekes (Canada)	
15:18	15:30	129	Cahora Bassa HVDC Scheme Operational Experience (Apollo Converter Station)	Sibongile Maphosa (South Africa)	
15:30	16:00	Networking Break			Exhibit Area
		B4 - Session 6 <i>Chair: Rick Valiquette (Canada)</i>		Presenter	MR-3
16:00	16:12	40	Validation of MMC Converter Real-Time Models with Field Tests	Sebastien Denetiere (France)	
16:12	16:24	48	Comparison of Mitigation Methods for Commutation Failure Reduction on the Brazilian Multi Infeed HVDC System	Felipe Pedroso (Brazil)	
16:24	16:36	52	HVDC Control Replica Development for Nelson River Bipole I & II	Zhibo Wang (Canada)	
17:00	18:30	Cocktail Reception			Carlton Lobby
18:30	22:00	Conference Banquet			York Ballroom 1

Time		Monday, October 2, 2017 – Parallel Technical Session D1			Room
07:00	18:00	Registration			East Concourse
07:00	08:00	Breakfast			2FGH, 2 <sup>nd</sup> Floor
07:00	16:00	Speaker Ready Room			Exec. Conf. Ctr.
09:00	16:00	Exhibitor Trade Show			MR 1-3N, 10-13
		D1 - Session 1: DC GIS, Field Grading, Space Charges <i>Chairs: Ralf Pietsch &amp; Johannes Seiler (Germany)</i>		Presenter	MR-8
08:00	08:15	139	Application Options and Electrical Field Studies as Basis for Adequate Testing of Gas-Insulated Systems for HVDC	Uwe Riechert (Switzerland)	
08:15	08:30	163	Influence of Extreme Temperature Conditions on the Gas-Solid Insulating System Under DC Voltage Stress	Karsten Juhre (Germany)	
08:30	08:45	143	Characterization of Fillers for HVDC Field Grading Materials	Maximilian Secklehner (Germany)	
08:45	09:00	110	Some Thoughts Regarding Prototype Installation Tests of Gas-Insulated HVDC Systems	Claus Neumann (Germany)	
09:00	09:30	Networking Break			Exhibit Area

		D1 - Session 2: UHV GIL, Aspects About Design and Testing, Ecological Properties of Gas Mixtures <i>Chairs: Ralf Pietsch &amp; Johannes Seiler (Germany)</i>		Presenter	MR-8
09:30	09:45	46	Developments in Testing and Verification of GIS	Robert le Roux (Ireland)	
09:45	10:00	18	The Insulation Design and Engineering Application of UHV GIL	Xianglian Yan (China)	
10:00	10:15	140	Behavior of Gaseous Dielectrics with Low Global Warming Potential Considering Partial Discharges and Electric Arcing	Bernhard Lutz (Germany)	
10:15	10:30	152	Characteristics of g3 – an Alternative to SF6	Yannik Kieffel (France)	
10:30	11:00	Networking Break			Exhibit Area
		D1 - Session 3: Simulations, Design Concept, Testing Cables; Space Charges in Fluids <i>Chairs: Ralf Pietsch &amp; Johannes Seiler (Germany)</i>		Presenter	MR-8
11:00	11:15	21	Simulation of Thermal Behavior of a 320 kV HVDC Cable Joint with Nonlinear Resistive Field Grading Under Impulse Voltage Stress	Rahid Hussain (Germany)	
11:15	11:30	161	A Design Path to Extruded XLPE Materials for 90C HVDC Applications	Tim Person (U.S.A.)	
11:30	11:45	98	Space Charge Behaviours of Insulating Paper Impregnated in Mineral Oil and Ester Fluids	Zhou Mu (U. K.)	
11:30	13:00	CIGRE Canada Women in Engineering Luncheon			MR 17
12:00	13:00	Lunch			2FGH, 2 <sup>nd</sup> Floor
		D1 - Session 4: PD, UHF Method, RIF Tests <i>Chairs: Ralf Pietsch &amp; Johannes Seiler (Germany)</i>		Presenter	MR-8
13:00	13:15	10	PD Detection Technology Based on UHF Method in Combination With Optical Fiber Sensors	Junhao Li (China)	
13:15	13:30	86	Application and Benefits of UHF On-Line Partial Discharge Monitoring in GIS	Hans-Dieter Schlemper (Switzerland)	
13:30	13:45	38	Integral Equation for Analysis of Partial Discharge Characteristics Based on Fluctuation of Residual Voltage and Delay Time	Tatsuki Okamoto (Japan)	
13:45	14:00	149	Experience From Corona and Radio Interference Voltage Tests Performed on HVDC Transmission Line Hardware	Nathan Jacob (Canada)	
14:00	14:30	Networking Break			Exhibit Area

		D1 - Session 5: Testing of AC & HVDC Cables, Assessment of OHL and Towers <i>Chairs: Ralf Pietsch &amp; Johannes Seiler (Germany)</i>		Presenter	MR-8
14:30	14:45	94	Investigating the Effectiveness of Different Test Methods Performing After-Laying Tests on Long XLPE MV and HV Cables	Sai Charan Gedela (Germany)	
14:45	15:00	164	High Voltage DC Test System for Factory and After Installation Tests of Long HVDC Cables	Ralf Pietsch (Germany)	
15:00	15:15	107	Progressive Failure of Overhead Electrical Conductors Due to Fretting Fatigue and Thermal Stresses	Karen Callery (Canada)	
15:15	15:30		Summary of Monday Session	Ralf Pietsch (Germany)	
15:30	16:00	Networking Break			Exhibit Area
17:00	18:30	Cocktail Reception			Carlton Lobby
18:30	22:00	Conference Banquet			York Ballroom 1

Time		Tuesday, October 3, 2017 – Parallel Technical Session A3/B4		Room
07:00	13:00	Registration		East Concourse
07:00	08:00	Breakfast		2FGH, 2 <sup>nd</sup> Floor
07:00	15:00	Speaker Ready Room		Exec. Conf. Ctr
09:00	16:00	Exhibitor Trade Show		MR 1-3N, 10-13
		A3/B4 - Session 1: DC Circuit Breaker Requirements <i>Chair: Hiroki Ito (CIGRE)</i>		Presenter
08:00	08:15	34	Short-Circuit Current Calculation in AC Networks in Case of HVDC Stations	Gerd Balzer (Germany)
08:15	08:30	9	Stress Analysis of HVDC Circuit Breakers for Defining Test Requirements and Its Implementation	Nadew Adisu Belda (The Netherlands)
08:30	08:45	124	Breaking Test for High Voltage DC Circuit Breaker	Sho Tokoyoda (Japan)
08:45	09:00	125	Mechanical Circuit Breaker Modelling for System-level, Real-time Protection System Simulations	Frederick Page (Japan)
09:00	09:30	Networking Break		Exhibit Area

		A3/B4 - Session 2: DC Circuit Breaker Development <i>Chair: Anton Janssen (The Netherlands)</i>		Presenter	MR-4
09:30	09:45	15	A Novel Commutation-Based Hybrid HVDC Circuit Breaker	Wei Shi (China)	
09:45	10:00	13	Transient Current Analysis of HVDC Smoothing Reactor Base on Energy Conservation Principle	Ji Feng (China)	
10:00	10:15	41	Hybrid DC Circuit BKR Using Gas-Discharge Switches for High-Voltage Switching	Colin Davidson (U.K.)	
10:15	10:30	105	Low Voltage Prototype Design and Testing of Ultra-Fast Disconnecter (UFD) for Hybrid DC CB	Dragan Jovicic (U. K.)	
10:30	10:45				
10:45	11:00	Networking Break			Exhibit Area
		A3/B4 – Session 3: Panel Discussion on DC Circuit Breaker <i>Chair: Christian Franck (Switzerland)</i>		Presenter	MR-4
11:00	11:45	PS 1	Recent Progress of HVDC CB Intended for Demonstration of Pilot HVDC Projects	Contributions by All Attendees	
		PS 2	R&D Developments (HVDC CB Requirements, Protection Schemes, Novel Technologies, Testing)		
11:45	12:00		Discussion and Summary		
12:00	13:00	Lunch			2FGH, 2 <sup>nd</sup> Floor
15:30	16:00	Networking Break / Prize Draw			Exhibit Area

Time		Tuesday, October 3, 2017 – Parallel Technical Session B4			Room
07:00	13:00	Registration			East Concourse
07:00	08:00	Breakfast			2FGH, 2 <sup>nd</sup> Floor
07:00	15:00	Speaker Ready Room			Exec. Conf. Ctr.
09:00	16:00	Exhibitor Trade Show			MR 1-3N, 10-13
		B4 - Session 7 <i>Chair: Carsten Bartzsch (Germany)</i>		Presenter	MR-3
08:00	08:12	32	Comparative Analysis of DC Line Fault Recovery Technology in VSC HVDC	Chongxue Jiang (China)	
08:12	08:24	42	Mitigating the Interaction Between VSC-HVDC Links When One Link is Subject to a DC Side Fault	Colin Davidson (U.K.)	
08:24	08:36	64	Overvoltages Due to Pole to Ground Faults on Mixed Overhead Line-Cable Transmission System of HVDC Symmetric Monopole Configuration	Rao Atmuri (Canada)	
08:36	08:48	83	Behaviour of the HVDC Links With MMC Technology During DC Cable Faults	Marlien Greve (Germany)	
08:48	09:00	118	- A feasibility Study of an SVC HVDC System to Improve the Robustness of Mexico and USA Electrical Interconnection	Rafael Castellanos (Mexico)	
09:00	09:30	Networking Break			Exhibit Area

		B4 - Session 8 <i>Chair: Abhay Kumar (Sweden)</i>		Presenter	MR-3
09:30	09:42	20	Type Test of Large Capacity $\pm$ 800kV UHVDC Valve	Sun Jian (China)	
09:42	09:54	37	Conducted Emission of Modular Multilevel Converters up to 30 MHz - Assessment of Impacting Converter Non-Idealities	Kevin Hermanns (Germany)	
09:54	10:06	138	Air-core Dry-type Reactors for Electric Power Systems - Design Considerations for High Current Connections	Alexander Grisenti (Austria)	
10:06	10:18	54	Damping Resistor Design Consideration	Yann Ellassad (France)	
10:18	10:30	88	Maritime Link - Enabling High Availability with a VSC HVDC Transmission	Peter Lundberg (Sweden)	
10:30	11:00	<b>Networking Break</b>			<b>Exhibit Area</b>
		B4 - Session 9 <i>Chair : Les Brand (Australia)</i>		Presenter	MR-3
11:00	11:12	66	AC Harmonics Filters and Reactive Power Compensation Design for Labrador Island Link $\pm$ 350kV HVDC Transmission Scheme	Gearoid o hEidhin (U.K.)	
11:12	11:24	96	Overvoltage Assessment for Insulation Coordination of HVDC Overhead Lines With MMC-VSC Technology	Maryam Salimi (Canada)	
11:24	11:36	146	Specification and Operational Experience of the New HVDC Link Between Poland and Lithuania	Michał Kosmecki (Poland)	
11:36	11:48	119	Remote Community Supply Using DC Transmission	Robert Whitehouse (U.K.)	
11:48	12:00				
12:00	13:00	<b>Lunch</b>			<b>2FGH, 2<sup>nd</sup> Floor</b>
		B4 - Session 10 <i>Chair: Nombuso Ramaite (South Africa)</i>		Presenter	MR-3
13:00	13:12	157	A Comparison of VSC DC Fault Protection Methods Suitable for Over-Head HVDC Lines	Azmy Gowaid (U.K.)	
13:12	13:24	22	Research on Architecture and Strategies for Control and Protection System of Nanjing Double-Circuit Line UPFC Project	Jiang Lu (China)	
13:24	13:36	78	Operating Experience of HVDC Links – Behaviour After Switching Events in the Onshore Grid	Masoumeh Koochack Zadeh (Germany)	
13:36	13:48	100	AC Dynamic System Equivalentents for use in Real Time Simulations of HVDCs	Oscar Amaya-Zepeda (Canada)	
13:48	14:00	156	Enhanced Voltage Support with VSC-HVDC During Unbalanced AC faults	Ewgenij Starschich (Germany)	
14:00	14:30	<b>Networking Break</b>			<b>Exhibit Area</b>



		B4 - Session 11 <i>Chair: Ting An (China)</i>		Presenter	MR-3	
14:30	14:42	7	A Probabilistic Approach for HVdc System Capital Project Prioritization	Dange Huang (Canada)		
14:42	14:54	111	Electro-osmosis Investigations Performed for the Manitoba Hydro Bipole III HVDC Keewatinohk Ground Electrode	Mark Stobart (Canada)		
14:54	15:06	114	Fault Staging On a 500kV Monopole HVDC Link Utilizing an Unmanned Aerial Vehicle	Ben Korbutiak (Canada)		
15:06	15:18	117	Dynamic Performance Evaluation of Static Var Compensators with Adaptive Control Systems Operating Electrically Close in Real Time Digital Simulator	Manfredo Veloso Borges Correia Lima (Brazil)		
15:18	15:30	141	Thyristors and Diodes for Modern HVDC Applications	Jens Przybilla (Germany)		
15:30	16:00	<b>Networking Break / Prize Draw</b>				Exhibit Area
		B4 - Session 12 <i>Chair: John McNichol (Canada)</i>		Presenter	MR-3	
16:00	16:12	85	Power System Studies Under Consideration of the Short Circuit Power of VSC HVDC Systems	Christian Rathke (Germany)		
16:12	16:24	56	Frequency Stability in Case of Interconnectors Trips and Impact of Frequency Stabilizer	Ervin Spahic (Germany)		
16:24	16:36	128	Adjust Coordinated of the Parameters of the Controllers PSS and POD for the Devices FACTS UPFC	Rodrigo Possidônio Noronha (Brazil)		

Time		Tuesday, October 3, 2017 – Parallel Technical Session D1			Room
07:00	13:00	Registration			East Concourse
07:00	08:00	Breakfast			2FGH, 2 <sup>nd</sup> Floor
07:00	15:00	Speaker Ready Room			Exec. Conf. Ctr.
09:00	16:00	Exhibitor Trade Show			MR 1-3N, 10-13
		D1 - Session 6: Composite and Glass Insulators <i>Chairs: Ralf Pietsch &amp; Johannes Seiler (Germany)</i>		Presenter	MR-8
08:00	08:15		Introduction to Second Day D1 Sessions	Ralf Pietsch (Germany)	
08:15	08:30	142	Evaluation of Composite Insulators with Internal Deterioration: Lessons Learned from Service and After Service Testing	Igor Gutman (Sweden)	
08:30	08:45	151	Key Properties and Performance of Silicone Coated Toughened Glass Insulators for Contaminated Environments	Jean Marie George (Canada)	
08:45	09:00	82	Round Robin Test to the Dielectric Type Testing of a 400 kV Insulator Set	Jan Lachman (Czech Republic)	
09:00	09:30	Networking Break			Exhibit Area

		D1 - Session 7: Surge Arresters, Carbon Nano Tubes, Transformer Insulations <i>Chairs: Ralf Pietsch &amp; Johannes Seiler (Germany)</i>		Presenter	MR-8	
09:30	09:45	159	Electrical Conductivity of Individual Multi-Walled Carbon NanoTube (MWCNT) and Fibers - Results of a Parallel Approach Combining Theoretical and Experimental Investigations	Christian Poumarede (France)		
09:45	10:00	97	Observation of and Response to Fast Flashover of FRP Surfaces	William McDermid (Canada)		
10:00	10:15	160	Mechanical Behaviour of Transformer Insulation Materials	Lars Schmidt (Germany)		
10:15	10:30	58	Partial Discharge Characteristics and Degradation Process Due to Partial Discharge of Oil Impregnated Paper Insulation System With an Oil Gap	Yuta Makino (Japan)		
10:30	11:00	<b>Networking Break</b>			<b>Exhibit Area</b>	
		D1 - Session 8: Aging of Insulators, Monitoring HVDC Projects, Health Index <i>Chairs: Ralf Pietsch &amp; Johannes Seiler (Germany)</i>		Presenter	MR-8	
11:00	11:15	113	Thermal Gradient Contribution to Aging Processes Within Insulation Material: Experimental Design and Implementation	Anton Vykhodtsev (Canada)		
11:15	11:30	120	Electrical Effects Measurement and Monitoring of Manitoba Hydro Bipole III Project	Dean Reske (Canada)		
11:30	11:45	95	Health Index Methodologies for Decision Making on Asset Maintenance and Replacement	Robert Ross (Netherlands)		
11:45	12:00					
12:00	13:00	<b>Lunch</b>			<b>2FGH, 2<sup>nd</sup> Floor</b>	
		D1 - Session 9: LI Withstand Test, Simulations, Testing of HVDC Converter Valves <i>Chairs: Ralf Pietsch &amp; Johannes Seiler (Germany)</i>		Presenter	MR-8	
13:00	13:15	108	Comparison of Measuring Systems Used in Impulse Puncture Withstand Test	Orsino Borges Filho (Brazil)		
13:15	13:30	150	A White Box Model for Transient Analysis of Transformer Windings Using Multiconductor Transmission Line (MTL) Theory and Finite Element Method Comparative Analysis of DC Line Fault Recovery Technology in VSC HVDC	Feras Fattal (Canada)		
13:30	13:45	162	Design and Testing for HVDC Converter Valves Used at High Altitude	Dong Wu (Sweden)		
13:45	14:00		Summary & Conclusion of D1 Sessions	Ralf Pietsch (Germany)		
14:00	14:30	<b>Networking Break</b>			<b>Exhibit Area</b>	
15:30	16:00	<b>Networking Break / Prize Draw</b>			<b>Exhibit Area</b>	